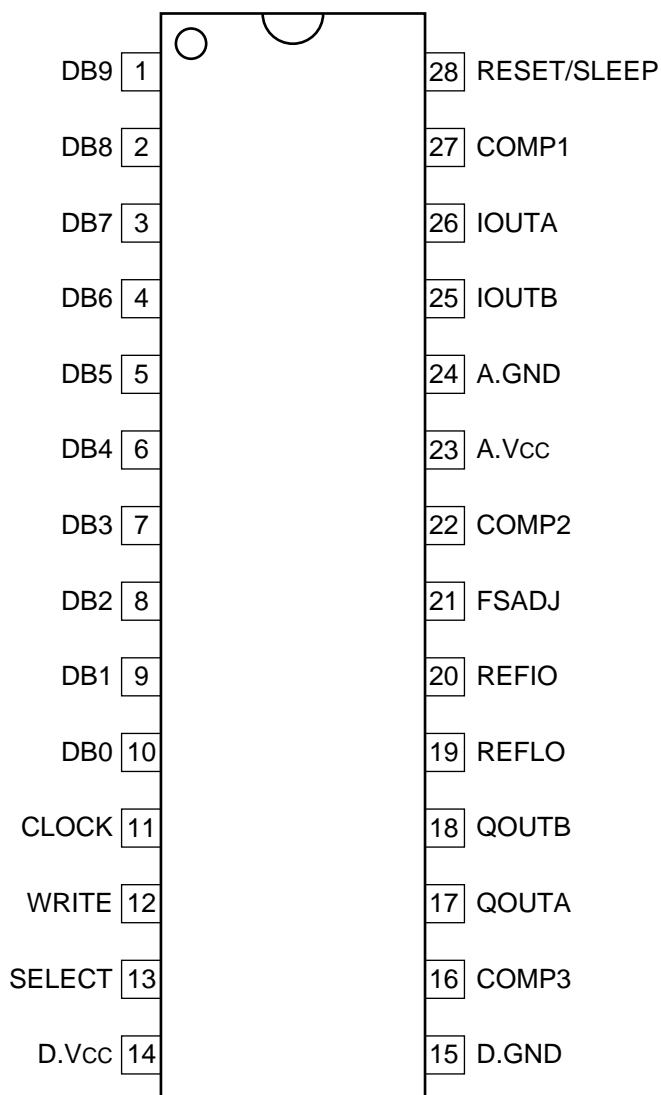


## 10-BIT DUAL CHANNEL D/A CONVERTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	DB9	8	I	DB2	15	—	D.GND	22	I	COMP2
2	I	DB8	9	I	DB1	16	I	COMP3	23	—	A.Vcc
3	I	DB7	10	I	DB0	17	O	QOUTA	24	—	A.GND
4	I	DB6	11	I	CLOCK	18	O	QOUTB	25	O	IOUTB
5	I	DB5	12	I	WRITE	19	O	REFLO	26	O	IOUTA
6	I	DB4	13	I	SELECT	20	I/O	REFIO	27	I	COMP1
7	I	DB3	14	—	D.Vcc	21	O	FSADJ	28	I	RESET/SLEEP

**INPUTS**

CLOCK : CLOCK  
 COMP1 : INTERNAL BIAS NODE FOR SWITCH DRIVER CIRCUITRY  
 COMP2 : BANDWIDTH/NOISE REDUCTION NODE  
 COMP3 : INTERNAL BIAS NODE FOR SWITCH DRIVER CIRCUITRY  
 DB0 - DB9 : DATA BIT  
 RESET/SLEEP : POWER DOWN CONTROL  
 SELECT : SELECT  
 WRITE : WRITE

**OUTPUTS**

FSADJ : FULL SCALE CURRENT OUTPUT ADJUST  
 IOUTA : I DAC CURRENT  
 IOUTB : I DAC COMPLEMENTARY CURRENT  
 QOUTA : Q DAC CURRENT  
 QOUTB : Q DAC COMPLEMENTARY CURRENT  
 REFLO : REFERENCE GROUND

**INPUT/OUTPUT**

REFIO : REFERENCE

